

Serial No. 09/045,041

Docket No. 122.1329/CJG



41(a)
p2
5-9

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hisanori FUJISAWA

Serial No.: 09/045,041

Group Art Unit: 2763

Filed: March 20, 1998

Examiner: H. Jones

For: METHOD AND APPARATUS FOR CARRYING OUT CIRCUIT
SIMULATION

AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

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Sir:

This is in response to the Office Action mailed December 3, 1999, and having a period for response set to expire on March 3, 2000. A Petition for a two-month extension of time, together with the requisite fee for same, is submitted herewith, thereby extending the period for response to May 3, 2000. Also enclosed with this response is an Information Disclosure Statement and accompanying PTO-1449 for Japanese Unexamined Patent Publication No. 10-040280, which corresponds to Japanese Patent Application No. 8-198074 referenced on page 2, lines 22-25 of the specification.

The following amendments and remarks are respectfully submitted.

IN THE SPECIFICATION:

Please ~~AMEND~~ the specification as follows:

Page 3, line 7, please delete "VDD" and insert therefore --VSS--.

Page ~~3~~, line ~~7~~, after "supply", please delete "VSS" and insert therefore --VDD--.